

1/3

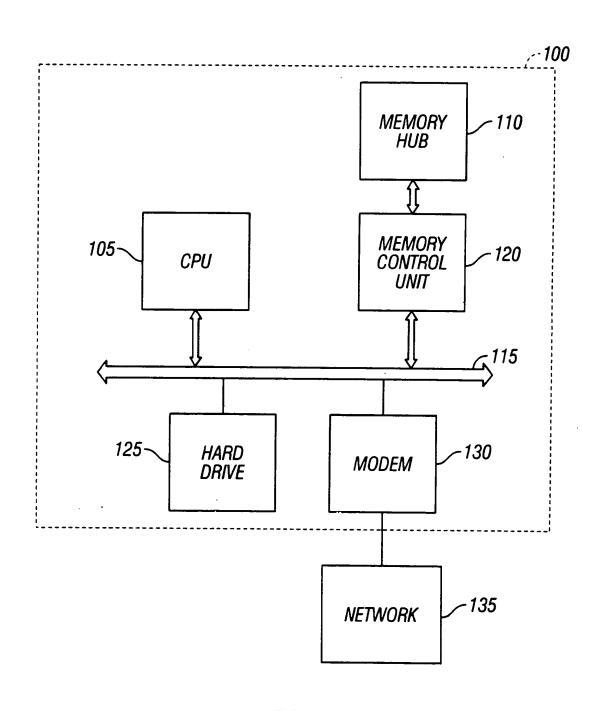
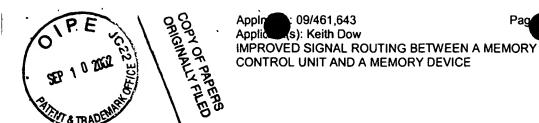


FIG. 1 (Prior Art)



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of 3

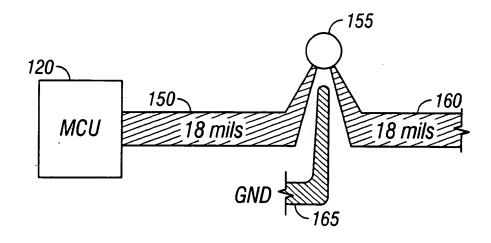


FIG. 2 (Prior Art)

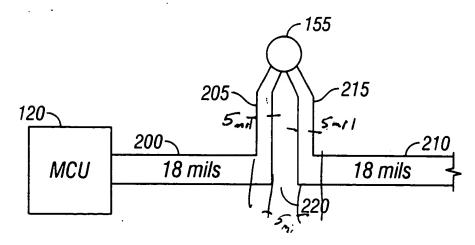
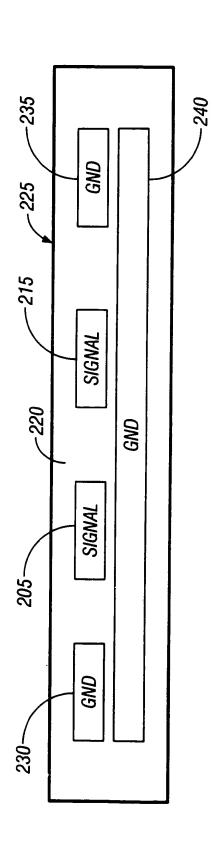


FIG. 3



Àg No.: 09/461,643 A. ant(s): Keith Dow IMPROVED SIGNAL ROUTING BETWEEN A MEMORY CONTROL UNIT AND A MEMORY DEVICE

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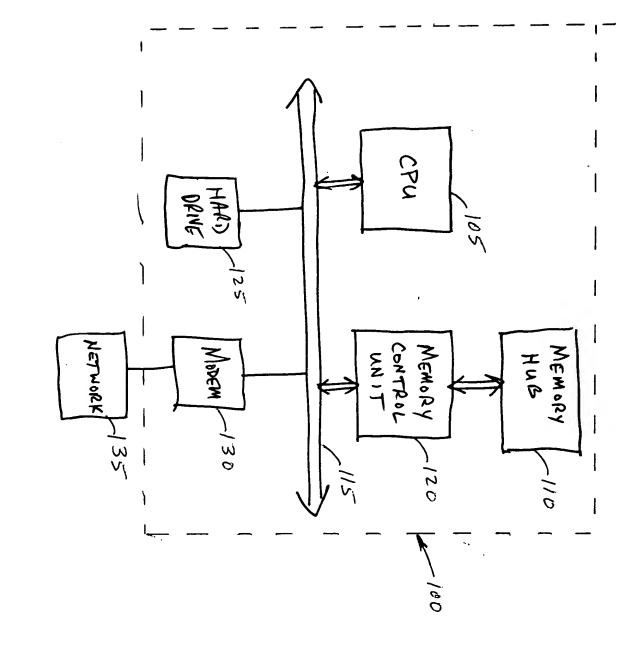
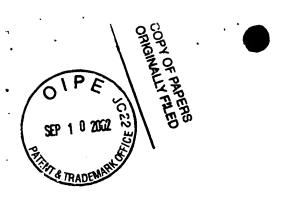
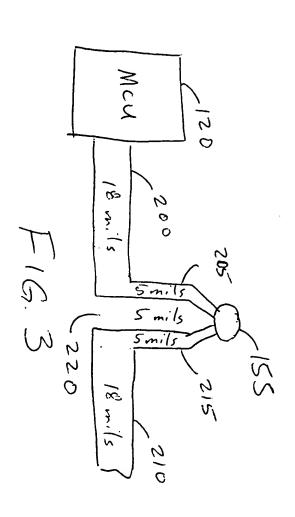


FIG. 1 PRIAR ART





McH 120 150 mils, -16.2 PRIOR ART 5ND / 165 091



